

**REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY
AND CHANGE OF CORRESPONDENCE ADDRESS**

I hereby revoke all previous powers of attorney given in the below-identified applications and patents.

I hereby appoint the practitioners associated with Customer Number: 38881.

Please change the correspondence address for the below-identified applications and patents to the address associated with Customer Number: 38881.

I am the assignee of record of the entire interest in each of the below-identified applications and patents. Statements under 37 CFR 3.73(b) for each of the respective applications/patents are enclosed with the submitted copies of this document.

Authorization to file this Revocation of Power of Attorney with New Power of Attorney and Change of Correspondence Address form listing multiple application and patent numbers rather than the assignee, Infineon Technologies North America Corp., signing a separate form for each application and patent was provided by Magdalen Y. Greenlief (tel.: 571-272-8850) of the Office of the Manual of Patent Examining Procedure. The original copy of this document is filed in Application No. 10/774,193, which was filed on February 6, 2004.

Appln. No.	Filing Date	Title	Inventor(s)
*10/712,473	11/12/2003	INTERRUPT AND TRAP HANDLING IN AN EMBEDDED MULTI-THREAD PROCESSOR TO AVOID PRIORITY INVERSION AND MAINTAIN REAL-TIME OPERATION	Ober, Robert E.; Arnold, Roger D.; Martin, Daniel F.; Norden, Erik K.
*11/130,552	5/16/2005	CONTROLLING OUT OF ORDER EXECUTION PIPELINES USING PIPELINE SKEW PARAMETERS	Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S.
10/386,974	3/12/2003	MULTIPLE DELAY LOCKED LOOP INTEGRATION SYSTEM AND METHOD	Zweck, Harald
10/396,063	3/24/2003	ZERO-OVERHEAD LOOP OPERATION IN MICROPROCESSOR HAVING INSTRUCTION BUFFER	Ahmad, Sagheer; Knoth, Matthias; Arnold, Roger D.
10/411,409	4/9/2003	NEW ARITHMETIC LOGIC UNIT WITH MERGED CIRCUITRY FOR COMPARISON, MINIMUM/MAXIMUM SELECTION AND SATURATION FOR SIGNED AND UNSIGNED NUMBERS	Griessing, Alexander M.
10/444,918	5/22/2003	CONFIGURABLE REAL-TIME TRACE PORT FOR EMBEDDED PROCESSORS	Ahmad, Sagheer; Ober, Robert E.
10/705,101	11/10/2003	MEMORY DEBUGGER FOR SYSTEM-ON-A-CHIP DESIGNS	Oberlaender, Klaus J.
10/714,137	11/13/2003	MACHINE INSTRUCTION FOR ENHANCED CONTROL OF MULTIPLE VIRTUAL PROCESSOR SYSTEMS	Arnold, Roger D.; Ober, Robert E.
10/774,193	2/6/2004	PROGRAM TRACING IN A MULTITHREADED PROCESSOR	Ober, Robert E.; Martin, Daniel F.; Arnold, Roger D.; Norden, Erik K.

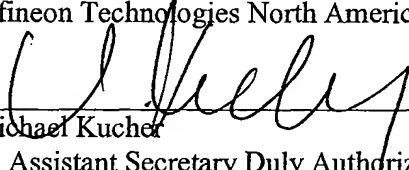
DOCSNY.190221.1

*Infineon Technologies North America is assignee of record of a *partial* interest of these applications/patents.

Appln. No.	Filing Date	Title	Inventor(s)
10/774,226	2/6/2004	THREAD ID IN A MULTITHREADED PROCESSOR	Norden, Erik K.; Ober, Robert E.; Arnold, Roger D.; Martin, Daniel F.
10/777,570	2/11/2004	FAST UNALIGNED MEMORY ACCESS SYSTEM AND METHOD	Oberlaender, Klaus J.; Norden, Erik K.
10/777,739	2/11/2004	CONFIGURABLE EMBEDDED PROCESSOR	Ober, Robert E.; Oberlaender, Klaus J.
10/777,863	2/11/2004	CONFIGURABLE MEMORY SYSTEM FOR EMBEDDED PROCESSORS	Oberlaender, Klaus J.; Haines, Ralph
11/114,734	4/25/2005	MULTICAST/BROADCAST EXTENSION TO A POINT-TO- POINT UNICAST-ONLY PACKET SWITCH SYSTEM	Lin, Jim; Kadamby, Vijayasimha

Patent No.	Issue Date	Title	Inventor(s)
*6,774,691	8/10/2004	HIGH RESOLUTION INTERLEAVED DELAY CHAIN	Han, Jonghee; Kim, Jung Pill
6,674,684	1/6/2004	MULTI- BANK CHIP COMPATIBLE WITH A CONTROLLER DESIGNED FOR A LESSER NUMBER OF BANKS AND METHOD OF OPERATING	Shen, William Wu
6,914,844	7/5/2005	DEEP POWER DOWN SWITCH FOR MEMORY DEVICE	Suh, Jungwon

Signature of Assignee of Record

Infineon Technologies North America Corp.	
	4/12/06
Michael Kucher Its Assistant Secretary Duly Authorized	Date